

Research scholar details in Research Center, Department of ECE.

| Sl. No. | Research scholar | Guide | Year of registration | Area | Status |
|---------|-------------------------|----------------------|----------------------|--------------------------------|-------------------------------------------------------------------------------------------------|
| 1 | Mr. Shridhar Devarmane | Dr. Rajeshwari Itagi | 2015 | Communication | Completed comprehensive viva. Published 2 papers in Computer Science and Information Journal |
| 2 | Mr. Ravi Hosamani | Dr. Yerriswamy T. | 2015 | Communication | Completed course work. |
| 3 | Mrs. Megha maine | Dr. Rajeshwari Itagi | 2016 | Communication | Completed course work |
| 4 | Mrs. Shreedevi Kulkarni | Dr. Manu T.M | 2016 | AI, image and video processing | Cleared 3 course works. Waiting for result of 1 subject. |
| 5 | Mr. Praveen H.L. | Dr. Yerriswamy T. | 2016 | Antenna | Completed course work |
| 6 | Mr. Vishwas Patil | Dr. Manu T. M. | 2018 | Image and video processing | Written 2 course work subjects. Waiting for results. |
| 7 | Mr. Darshankumar Billur | Dr. Manu T.M | 2018 | Image and video processing | Written 2 course work subjects. Waiting for results. |
| 8 | Mrs. Vinuta | Dr. Manu T.M | 2019 | VLSI and video processing | Registered |

Research scholar details in other colleges.

| Sl. No. | Research scholar | Guide | Year of registration | Area | Status |
|---------|------------------|------------------------------------|----------------------|---------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 1 | Mr. Tejas K. R. | Dr. H. C. Nagaraj, NMIT, Bengaluru | 2013 | Image and video processing | Writing thesis. Published 5 papers, 1 communicated. 1 IEEE Conference paper. 1 SCOPUS indexed journal 1 WoS indexed journal 1 Google scholar journal. 1 open access journal. |
| 2 | Mr. Rakesh H. M. | Dr. G. S. Sunitha, BIET, Davangere | 2015 | VLSI | Completed comprehensive viva Published 6 papers. 5 Google scholar journals. 1 IEEE conference paper. |
| 3 | Mr. Chetan V. S. | Dr. B. S. Anami, KLEIT, Hubballi | 2015 | Image processing and machine learning | Completed comprehensive viva. Published in 2 international conference papers. |



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Dept of Electronics & Communication Engg.



List of Publications

Mr. Tejas K. R.

1. Tejas K. rayangoudar and Dr. H.C. Nagaraj, "Significance Of Effective Local Gradient Distribution Technique With Eft On Multi-View And Cloth Invariant Gait Recognition," in International Journal of Future Generation Communication and Networking *Web of Science*, Vol. 13, No. 3, (2020), pp. 243- 252.
2. Tejas K. rayangoudar and Dr. H.C. Nagaraj, "CHOG based EFD for geometric shape retrieval of images for cloth and object invariant Gait recognition," in International Journal of Recent Technology and Engineering *Scopus Indexed*, 2277-3878, Volume-8 Issue-4, November 2019.
3. Tejas K. rayangoudar and Dr. H.C. Nagaraj, Effective Distribution Of Local Intensity Gradient Technique For View Invariant Gait Recognition, "2nd International Conference on Intelligent Communication and Computational Techniques *IEEE, Scopus Indexed Journal*, p187-191, 2019.
4. Tejas K. rayangoudar and Dr. H.C. Nagaraj, "A review on Gait methodology and challenges," International Conference on Recent Advancements in Engineering & Technology, pp. 48-52, 2017.

Mr. Shridhar Devamane

1. Mr. Shridhar Devamane, Dr. Rajeshwari Itagi, "Recurrent Neural Network Based Turbo Decoding Algorithms for Different Code Rates", **Elsevier Journal: Journal of King Saud University - Computer and Information Sciences**, 2020,

Mr. Rakesh Hiremath

1. Dr. G.S. Sunitha, Rakesh H.M, "Design and Implementation of adder architectures and analysis of performance metrics", International Journal of Electronics and Communication Engineering and Technology (IJECET), Volume 8, Issue 5, pp.1-6, Sep-Oct2017.
2. Rakesh H.M, Dr. G.S. Sunitha, "Design and Implementation of Vedic and Booth multiplier on spartan 3", International Journal of VLSI Design, Microelectronics and Embedded System, Volume 2, Issue 2 ,pp.34-40, Dec 2017.
3. Rakesh H.M, Dr. G.S. Sunitha, "Comparative analysis of 16 x 16 bit Vedic and Booth multipliers", World Journal of Technology, Research and Engineering(WJTER), Volume 3, Issue 1,pp.305-313, Jan 2018.
4. Rakesh H.M, Dr. G.S. Sunitha, "Performance Comparison of Conventional multiplier with Vedic Multiplier using ISE simulator", International Journal of Engineering and Manufacturing Science, ISSN 2249-3115 Volume 8, Number 1 ,pp. 95-103,2018.
5. Rakesh H.M, Dr. G.S. Sunitha, " A Survey on Architectures of MAC units for DSP Applications, " European Journal of Advances in Engineering and Technology", Volume 6, Issue 4, pp.36-39,2019.



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6. Rakesh H.M, Dr. G.S. Sunitha, “Design and implementation of Novel 32-Bit MAC unit for DSP Applications, “IEEE International Conference for Emerging Technology”, June 5-7, 2020.