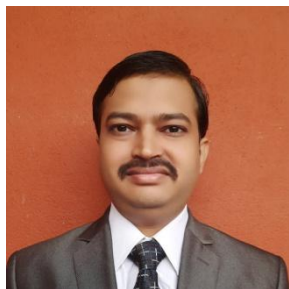


KLE Institute of Technology, Hubballi 580 027
Faculty Profile



Name: Vishwas Patil

1.	Educational, professional qualifications, and trainings				
1.1.	Educational Qualification (from highest to basic)				
S.N.	Degree	University / College	Discipline	Year of Passing	Class obtained
a)	Ph. D (Pursuing)	VTU, Belgaum	Electronics and Communication Engg.	--	--
b)	M.Tech	NITK, Surathkal	VLSI Design	2011	8.94 (CGPA)
c)	B.E	AEC, Bhatkal	Electronics and Communication Engg.	2006	69.3%
1.2.	Training programmes attended				
S.N.	Subject Area of Training	Organization	Place	Period / Duration	
a)	FPGA based System Design: A SoC approach	C-DAC	Bengaluru	10 days	
b)	System Design using SystemVerilog	VTU	Belagavi	4 days	
1.3.	Membership of National and International Professional Bodies/Organisations				
S.N.	Name of Professional Body/Organization	Place		Membership Category	
a)	MISTE	Bengaluru		Life time	
b)	IEEE			Professional	
c)					
1.4.	Technical Papers/Books Published in National / International Events / Journals				
a)	<p>Standard Format of Citation of Papers, Nomographs and books to be used.</p> <p>Papers:</p> <p>Vishwas Patil (2015). "FPGA Implementation of Precise Time Protocol for Smart Grid Communication." <i>National conference on Advances in Electrical Engineering</i>, NMAMIT, Nitte.</p> <p>Vishwas Patil (2016). "Thin film PV cells highlights, challenges and approach." <i>National Conference on Recent Trends in Mechanical and Electrical Engineering</i>, AITM, Bhatkal.</p> <p>Somshekhar R Puranmath, Vishwas Patil, Shivaraj S T (2017). "Hardware Efficient Image Stegnography", <i>International Journal of Engineering Development and Research</i>, 5(3), P 914-919</p>				
1.5.	Language skills (ability to)		Speak:	Read / Write	

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		English, Hindi, Kannada, Marathi.	English, Hindi, Kannada, Marathi.
2.	Employment Information and Professional Experience till date (provide latest to first).		
2.1.	Employment Information (Provide information in the following order: Job title, Employer, Dates (from – to), Responsibilities, highlight the work undertaken that best illustrates capability to handle the tasks assigned).		
a)	Job title	Assitant Professor	
	Employer	Anjuman Institute of Technology, Bhatkal	
	Dates (from – to)	29-08-2006 to 01-08-2016	
	Responsibilities		
b)	Job title	Assistant Professor	
	Employer	K.L.E. Institute of Technology, Hubballi	
	Dates (from – to)	08-08-2016 – till date	
	Responsibilities	ISO internal Auditor, ISO department co-ordinator, NMEICT Remote Center co-ordinator, Advitiya deptment co-ordinator	
c)	Job title		
	Employer		
	Dates (from – to)		
	Responsibilities		
2.2.	Public Service & Volunteer Work		
a)			
b)			
c)			
2.3.	Other professional achievements such as any awards, special skills, etc.		
a)			
b)			
c)			
3.	Any other information		
3.1.	Strengths		
4.	General information		
4.1.	Name	Vishwas Patil	
4.2.	Gender	Male	
4.3.	Nationality	Indian	
4.4.	Date of birth	19-07-1984	
4.5.	Contact address	#18, C/o N B Kmmar, Kumarpark, Hubballi	
4.6.	Phone / mobile number	9611030558	
4.7.	Email	v.bk156@gmail.com	

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Date	Full name	Signature