

KLE Institute of Technology, Hubballi 580 027
Faculty Profile




Name: Rakesh H.M

1. Educational, professional qualifications, and trainings					
1.1. Educational Qualification :Mtech					
S.N.	Degree	University / College	Discipline	Year of Passing	Class obtained
a)	B.E	VTU/RYMEC, BELLARY	ECE	2009	FC
b)	M.TECH	VTU/BVBCET	Vlsi design and testing	2012	FCD
1.2. Training programmes attended					
S.N.	Subject Area of Training		Organization	Place	Period / Duration
a)	FDP on signal processing		KLETECH	Hubballi	15 th -20 th july/1 week
b)	FDP on CADENCE		KLEIT	Hubballi	11 th -13 th may 2019/3 days
c)					
1.3. Membership of National and International Professional Bodies/Organisations					
S.N.	Name of Professional Body/Organization		Place		Membership Category
a)	IAENG		Hongkong		Life time member
1.4. Technical Papers/Books Published in National / International Events / Journals					
a)	<ol style="list-style-type: none"> 1. Dr. G.S. Sunitha, Rakesh H.M, “Design and Implementation of adder architectures and analysis of performance metrics”, International Journal of Electronics and Communication Engineering and Technology (IJECE), Volume 8, Issue 5, pp.1–6, Sep-Oct2017. 2. Rakesh H.M, Dr. G.S. Sunitha, “Design and Implementation of Vedic and Booth multiplier on spartan 3”, International Journal of VLSI Design, Microelectronics and Embedded System, Volume 2, Issue 2 ,pp.34-40, Dec 2017. 3. Rakesh H.M, Dr. G.S. Sunitha, “Comparative analysis of 16 x 16 bit Vedic and Booth multipliers”, World Journal of Technology, Research and Engineering(WJTER), Volume 3, Issue 1,pp.305-313, Jan 2018. 4. Rakesh H.M, Dr. G.S. Sunitha, “Performance Comparison of Conventional multiplier with Vedic Multiplier using ISE simulator”, International Journal of Engineering and Manufacturing Science, ISSN 2249-3115 Volume 8, Number 1 ,pp. 95-103,2018. 5. Rakesh H.M, Dr. G.S. Sunitha, “ A Survey on Architectures of MAC units for DSP Applications, “ European Journal of Advances in Engineering and 				

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	Technology”, Volume 6, Issue 4, pp.36-39, 2019.	
	6. Rakesh H.M, Dr. G.S. Sunitha, “Design and implementation of Novel 32-Bit MAC unit for DSP Applications, “IEEE International Conference for Emerging Technology”, June 5-7, 2020.	
1.5.	Language skills (ability to)	Speak:
		Kannada
		English
		Hindi
	Read / Write	
	Read/Write	
	Read/Write	
	Read/Write	
2.	Employment Information and Professional Experience till date (provide latest to first).	
2.1.	Employment Information	
a)	Job title	Assistant Professor
	Employer	KLEIT, Hubballi
	Dates (from – to)	01/8/2023 – till date
	Responsibilities	Phoenix Coordinator
b)	Job title	
	Employer	
	Dates (from – to)	
	Responsibilities	
c)	Job title	
	Employer	
	Dates (from – to)	
	Responsibilities	
2.2.	Public Service & Volunteer Work	
a)	-	
2.3.	Other professional achievements such as any awards, special skills, etc.	
a)	-	
3.	Any other information	
3.1.	Strengths	Dedicated, Determined, Disciplined & Decisive
4.	General information	
4.1.	Name	Rakesh H.M
4.2.	Gender	Male
4.3.	Nationality	Hindu Jangam
4.4.	Date of birth	30/01/1987
4.5.	Contact address	#6, Sri Guru Kottureshwara Swamy Krupa, Shakti Colony, 2 nd stage, Vishveshwara Nagar, Hubballi
4.6.	Phone / mobile number	7259333412
4.7.	Email	hiremath.rakesh99@gmail.com

12/07/2020	Rakesh Hiremath	
Date	Full name	Signature

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